Amendments to the Specification:

Please replace the paragraph beginning at page 9, line 12 with the following amended paragraph:

Figs. 14A to 14C are diagrams showing the constitution of a shift ~~resister~~register circuit;

Please replace the paragraph beginning at page 29, line 30 with the following amended paragraph:

In an active type display device, high speed operation is required to the peripheral drive circuit partly from the view-point of suppressing flickering and the like by speeding up the response of the display image plane. High speed operation is particularly required to the shift ~~resister~~register circuits and counter circuits which perform the clock operation.

Please replace the paragraph beginning at page 30, line 4 with the following amended paragraph:

Fig. 14A shows a shift ~~resistor~~register circuit constituting a gate driver portion. The function of the shift ~~resistor~~register circuit is to select the gate lines arrayed in the pixel region sequentially (or one by one). Thus, if the operation speed of the shift ~~resistor~~register circuit should be low, the selection of a gate line consumes time as to finally elongate the time necessary for the completion of a single field (or a single frame) in the display image plane. Thus, flickering occurs on the image plane.

Please replace the paragraph beginning at page 30, line 11 with the following amended

The shift ~~resistor~~register circuit described above is constructed basically by a clocked inverter circuit shown in Fig. 14B and an inverter circuit with reference to Fig. 14C. Since both circuits with reference to Figs. 14B and 14C are constructed by CMOS circuits, the present invention is applicable to the CMOS circuits.

Please replace the paragraph beginning at page 36, line 16 with the following amended paragraph:

The most striking future of the semiconductor manufactured in accordance with the present example is that the semiconductor excels in high speed operation. Thus, the semiconductor obtained in the present example is particularly suitable for use where high speed operation is required, e.g., as a peripheral drive circuit by constituting a CMOS circuit, and particularly, as a shift ~~resister~~register circuit.